

Abstract of the Disclosure

A data output circuit which outputs data on an internal bus line onto an external bus line includes a comparator, inverter, and controller. The comparator compares data on the external bus line and data to be
5 output on the internal bus line. The inverter outputs a signal obtained by inverting the data on the internal bus line when the number of changed bits exceeds half the total number of bits on the basis of an output result from the comparator. The controller outputs an
10 inversion display signal representing that the data has been inverted. A data output method is also disclosed.